

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Gil Naveh GROUP ART UNIT: 2181
APPLN. NO.: 10/675,640 EXAMINER: Vincent Lai
FILED: 09-30-2003
TITLE: METHOD AND SYSTEM FOR PROCESSING A SEQUENCE OF INSTRUCTIONS
IN MULTIPLE EXECUTION STAGES

Certificate of Submission

I hereby certify that this correspondence is being submitted to the
USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States
Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal
Framework for EFS Web".

July 20, 2007
Date of Submission

/Dora Hudgins/
Signature

Dora Hudgins
Printed Name of Person Signing Certificate

**NOTICE OF APPEAL FROM THE EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted,

/Ranjeev Singhl

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